The Designer's Guide to VHDL, Third Edition (Systems on Silicon)

By Peter J. Ashenden

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VHDL, the IEEE standard hardware description language for describing digital electronic systems, has recently been revised. *The Designer's Guide to VHDL* has become a standard in the industry for learning the features of VHDL and using it to verify hardware designs. This third edition is the first comprehensive book on the market to address the new features of VHDL-2008.

- First comprehensive book on VHDL to incorporate all new features of VHDL-2008, the latest release of the VHDL standard
- Helps readers get up to speed quickly with new features of the new standard
- Presents a structured guide to the modeling facilities offered by VHDL
- Shows how VHDL functions to help design digital systems
- Includes extensive case studies and source code used to develop testbenches and case study examples
- Helps readers gain maximum facility with VHDL for design of digital systems

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Review
"The second edition of The Designer's Guide to VHDL sets a new standard in VHDL texts. I am certain that you will find it a very valuable addition to your library."
--From the foreword by Paul Menchini, Menchini & Associates

About the Author

Peter J. Ashenden received his B.Sc.(Hons) and Ph.D. from the University of Adelaide, Australia. He was previously a senior lecturer in computer science and is now a Visiting Research Fellow at the University of Adelaide. His research interests are computer organization and electronic design automation. Dr. Ashenden is also an independent consultant specializing in electronic design automation (EDA). He is actively involved in IEEE working groups developing VHDL standards, is the author of *The Designer's Guide to VHDL* and *The Student's Guide to VHDL* and co-editor of the Morgan Kaufmann series, Systems on Silicon. He is a senior member of the IEEE and a member of the ACM.

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