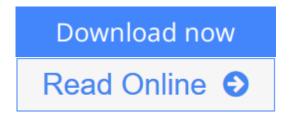# An Introduction to Digital Computer Design

By V. Rajaraman, T. Radhakrishnan

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This highly acclaimed, well established, book now in its fifth edition, is intended for an introductory course in digital computer design for b. Sc. Students of computer science, b. Tech. Students of computer science and engineering, and bca/mca students of computer applications. A knowledge of programming in c or java would be useful to give the student a proper perspective to appreciate the development of the subject. The first part of the book presents the basic tools and developes procedures suitable for the design of digital circuits and small digital systems. It equips students with a firm understanding of logic principles before they study the intricacies of logic organization and architecture of computers in the second part. Besides discussing data representation, arithmetic operations, boolean algebra and its application in designing combinatorial and sequential switching circuits, the book introduces the algorithmic state machines which are used to develop a hardware description language for the design of digital systems. The organization of a small hypothetical computer is described to illustrate how instruction sets are evolved. Real computers (namely, pentium and mips machines) are described and compared with the hypothetical computer. After discussing the features of a cpu, i/o devices and i/o organization, cache and virtual memory, the book concludes with a new chapter on the use of parallelism to enhance the speed of computers. Besides, the fifth edition has new material in cmos gates, msi/alu and pentium5 architecture. The chapter on cache and virtual memory has been rewritten.

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